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Complete if Known

Application Number	10/634,268
Filing Date	August 5, 2003
First Named Inventor	Jin-Man Han
Art Unit	
Examiner Name	
Attorney Docket Number	P2341-C-1

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
DL		HIRANDO, H., et al., 2V/100-ns 1T/1C Nonvolatile Ferroelectric Memory Architecture with Bitline-Driven Read Scheme and Nonrelaxation Reference Cell, IEEE Journal of Solid-State Circuits, 1997, pages 649- 54, Vol. 32, No. 5	

Examiner Signature		Date Considered	4/04
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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